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2. Pin Definitions 2. Pin Definitions Figure 2-1 shows the pin layout for 32-pin QFN package. § Figure 2-1: Pin Layout (Top View) Table 2-1 lists the definitions and functions of each pin.

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ESP8266 Datasheet Note: GPIO2, GPIO0, MTDO can be configurable as 3-bit SDIO mode. 2.2. Electrical Characteristics Table 3 ESP8266EX Electrical Characteristics 2.3. Power Consumption The following current consumption is based on 3.3V supply, and 25 °C ambient, using internal regulators. Measurements are done at antenna port without SAW filter. All the transmitter's measurements are based

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