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prefer to call it MPSoC (multi-processor system-on-chip) or CMP (chip multi- processor), no doubt this revolution affects large domains of both computer science and electronics, and it poses many new interdisciplinary challenges.

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In Retargetable Processor System Integration into Multi-Processor System-on-Chip Platforms, such originally independent approaches are combined in order to enable the development of highly optimized programmable platforms. The first chapters of this book summarize the state of the art in all three involved fields separately: general system level design, communication modeling, and processor modeling. The main chapters then present a methodology and the associated tooling for enabling design

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Retargetable processor system integration into multi-processor system-on-chip platforms. Summary: This book presents a methodology and the associated tooling for enabling design space exploration as well as a successive refinement flow for the design of optimized MP-SoCs with a high degree of automation.

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