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Chapter 1 Introduction 1.1 Themes1 From its beginnings in the late nineteenth century, electrical engineering has blossomed from focusing on electrical circuits for power, telegraphy and telephony to focusing on a much broader range of disciplines.

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1. Overview. This document examines the success of the widely adopted PCI bus and describes the higher-performance next generation of I/O interconnect technology PCI Express that will serve as a standard local I/O bus for a wide variety of future computing platforms.

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The P/F bit, Poll/Final is a single bit with two names. It is called Poll when set by the primary station to obtain a response from a secondary station, and Final when set by the secondary station to indicate a response or the end of transmission.

### **Plug and Play BIOS Specification v1 - Intel**

Plug and Play BIOS Specification 1.0A Page 5 This is the key consideration in a system BIOS. It is considered unacceptable to change the architecture of a System BIOS to prevent the thousands of ISA cards and software programs that

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